**entity** bufif1\_vhdl **is**

**port** (buf\_in, control,: **in** Std\_Logic; buf\_out: **out** Std\_Logic);

**end** bufif1\_vhdl;

**architecture** Dataflow **of** bufif1\_vhdl **is**

**begin**

buf\_out <= buf\_in **when** control = '1' **else** 'z';

**end** Dataflow;

**entity** notif1 **is**

**port** (not\_in, control: **in** Std\_Logic; not\_out: **out** Std\_Logic);

**end** notif1;

**architecture** Dataflow **of** notif1 **is**

**begin**

not\_out <= **not** (not\_in) when control = '1' **else** z;

**end** **architecture**;